

DATA SHEET

TDA1597

**IF amplifier/demodulator for FM
radio receivers**

Product specification
Supersedes data of May 1994
File under Integrated Circuits, IC01

1997 Feb 27

IF amplifier/demodulator for FM radio receivers

TDA1597

FEATURES

- Balanced limiting amplifier
- Balanced coincidence demodulator
- Two open-collector stop pulse outputs for microcomputer tuning control
- Simulated behaviour of ratio detector (internal field strength and detuning dependent voltage for dynamic AF signal muting)
- Mono/stereo blend field strength indication control voltage
- AFC output
- 3-state mode switch for FM-MUTE-ON, FM-MUTE-OFF and FM-OFF
- Internal compensation of AF signal Total Harmonic Distortion (THD)
- Built-in hum and ripple rejection circuits.

GENERAL DESCRIPTION

The TDA1597 provides IF amplification, symmetrical quadrature demodulation and level detection for quality home and car FM radio receivers and is suitable for mono and stereo reception. It may also be applied to common front-ends, stereo decoders and AM receivers circuits.

All pin numbers mentioned in this data sheet refer to the SO version (TDA1597T) unless otherwise specified.

QUICK REFERENCE DATA

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------------|---|--|------|------|------|--------------|
| V_P | supply voltage | | 7.5 | 8.5 | 12 | V |
| I_P | supply current ($I_2 = I_7 = 0$) | | – | 20 | 26 | mA |
| $V_{iIF(rms)}$ | IF input sensitivity for limiting on pin 20 (RMS value) | | 14 | 22 | 35 | μ V |
| $V_{oAF(rms)}$ | AF output signal on pin 4 (RMS value) | | 180 | 200 | 220 | mV |
| S/N | signal-to-noise ratio | $f_m = 400$ Hz; $\Delta f = \pm 75$ kHz | – | 82 | – | dB |
| THD | total harmonic distortion | $f_m = 1$ kHz; $\Delta f = \pm 75$ kHz | – | 0.1 | 0.3 | % |
| | | K2 adjustment and FM-MUTE-OFF | – | 0.07 | 0.25 | % |
| T_{amb} | operating ambient temperature | | –40 | – | +85 | $^{\circ}$ C |

ORDERING INFORMATION

| TYPE NUMBER | PACKAGE | | |
|-------------|---------|--|----------|
| | NAME | DESCRIPTION | VERSION |
| TDA1597 | DIP18 | plastic dual in-line package; 18 leads (300 mil) | SOT102-1 |
| TDA1597T | SO20 | plastic small outline package; 20 leads; body width 7.5 mm | SOT163-1 |

IF amplifier/demodulator for FM radio receivers

TDA1597

BLOCK DIAGRAM

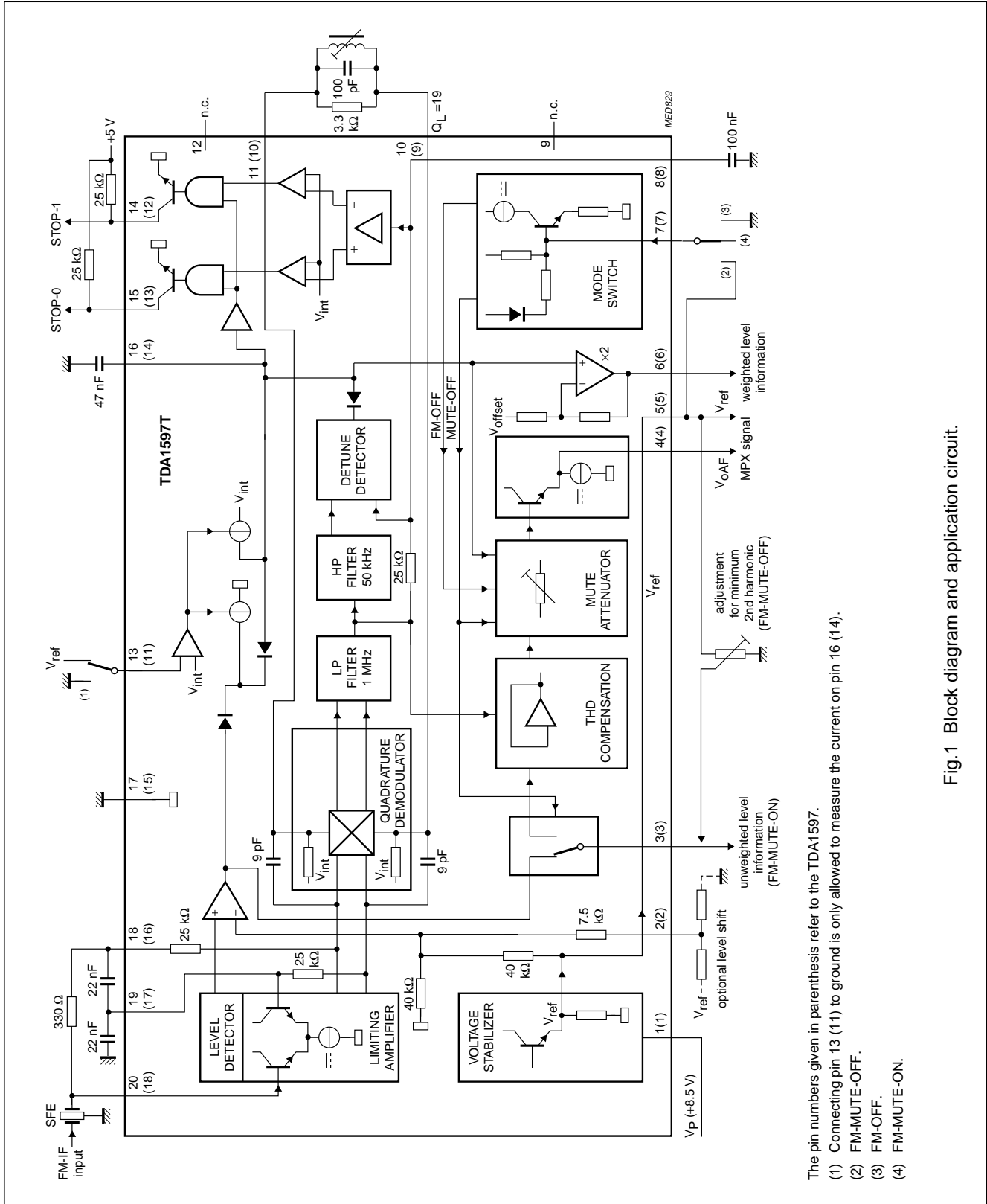


Fig.1 Block diagram and application circuit.

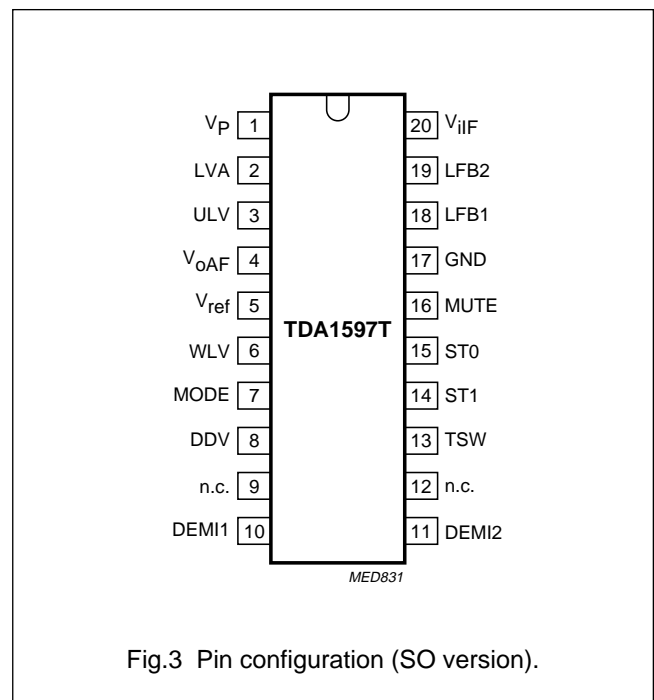
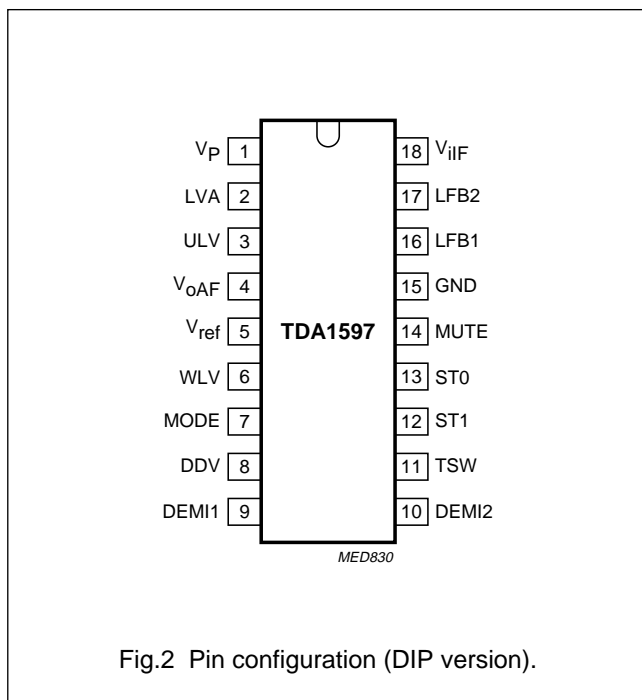
The pin numbers given in parenthesis refer to the TDA1597.
 (1) Connecting pin 13 (11) to ground is only allowed to measure the current on pin 16 (14).
 (2) FM-MUTE-OFF.
 (3) FM-OFF.
 (4) FM-MUTE-ON.

IF amplifier/demodulator for FM radio receivers

TDA1597

PINNING

| SYMBOL | PIN | | DESCRIPTION |
|------------------|-------|------|---------------------------------------|
| | DIP18 | SO20 | |
| V _P | 1 | 1 | supply voltage (+8.5 V) |
| LVA | 2 | 2 | level adjustment for stop condition |
| ULV | 3 | 3 | unweighted level output/K2 adjustment |
| V _{oAF} | 4 | 4 | audio frequency output (MPX signal) |
| V _{ref} | 5 | 5 | reference voltage output |
| WLV | 6 | 6 | weighted level output |
| MODE | 7 | 7 | mode switch input |
| DDV | 8 | 8 | detune detector voltage |
| n.c. | – | 9 | not connected |
| DEMI1 | 9 | 10 | demodulator input 1 |
| DEMI2 | 10 | 11 | demodulator input 2 |
| n.c. | – | 12 | not connected |
| TSW | 11 | 13 | tau switch input |
| ST1 | 12 | 14 | STOP-1, stop pulse output 1 |
| ST0 | 13 | 15 | STOP-0, stop pulse output 0 |
| MUTE | 14 | 16 | muting voltage |
| GND | 15 | 17 | ground (0 V) |
| LFB1 | 16 | 18 | IF limiter feedback 1 |
| LFB2 | 17 | 19 | IF limiter feedback 2 |
| V _{iIF} | 18 | 20 | IF signal input |



IF amplifier/demodulator for FM radio receivers

TDA1597

FUNCTIONAL DESCRIPTION

The limiter amplifier has five stages of IF amplification using balanced differential limiter amplifiers with emitter follower coupling.

Decoupling of the stages from the supply voltage line and an internal high-ohmic DC feedback loop gives a very stable IF performance. The amplifier gain is virtually independent of changes in temperature.

The FM demodulator is fully balanced and contains two cross-coupled differential amplifiers.

The quadrature detection of the FM signal is performed by direct feeding of one differential amplifier from the limiter amplifier output and the other via an external 90 degrees phase shifting network. The demodulator has a good stability and a small zero cross-over shift. The bandwidth of the demodulator output is restricted by an internal low-pass filter to approximately 1 MHz. Non-linearities, which are introduced by demodulation, are compensated for by the THD compensation circuit. For this reason, the demodulator resonance circuit (between pins 10 and 11) must have a loaded Q-factor of 19.

Consequently, there is no need for the demodulator tuned circuit to be adjusted for minimum distortion. Adjustment criterion is a symmetrical stop pulse. The control voltage for the mute attenuator (pin 16) is derived from the values of the level detector and the detuning detector output signals. The mute attenuator has a fast attack and a slow decay, determined by the capacitor connected to pin 16. The AF signal is fed via the mute attenuator to the output (pin 4). A weighted control voltage (pin 6) is obtained from the mute attenuator control voltage via a buffer amplifier that introduces an additional voltage shift and gain.

The level detector generates a voltage output signal proportional to the amplitude of the input signal. The unweighted level detector output signal is available in the FM-MUTE-ON condition (mode switch).

The open-collector tuning stop output voltages STOP-0 and STOP-1 (pins 15 and 14) are derived from the detuning and the input signal level. Pins 14 and 15 may be tied together if only one tuning-stop output is required.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|--------------|--|------------|-------|-------|------|
| V_P | supply voltage | | -0.3 | +13 | V |
| V_n | voltage at pins 2, 4, 5, 6, 10, 11 and 16 | | -0.3 | +10 | V |
| | voltage at pins 3, 7, 8, 14, 15, 18, 19 and 20 | | -0.3 | V_P | V |
| V_{13} | voltage on pin 13 | | - | 6 | V |
| $I_{14, 15}$ | current at pins 14 and 15 | | - | 2 | mA |
| P_{tot} | total power dissipation | | - | 360 | mW |
| T_{stg} | storage temperature | | -55 | +150 | °C |
| T_{amb} | operating ambient temperature | | -40 | +85 | °C |
| V_{es} | electrostatic handling | | | | |
| | all pins except pins 5 and 7 | note 1 | -2000 | +2000 | V |
| | pin 5 | note 1 | -2000 | +800 | V |
| | pin 7 | note 1 | -2000 | +1000 | V |

Note

- Equivalent to discharging a 100 pF capacitor through a 1.5 kΩ series resistor.

IF amplifier/demodulator for FM radio receivers

TDA1597

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | VALUE | UNIT |
|---------------|---|-------|------|
| $R_{th\ j-a}$ | thermal resistance from junction to ambient in free air | | |
| | SOT102-1 | 80 | K/W |
| | SOT163-1 | 90 | K/W |

CHARACTERISTICS

$V_P = 8.5\text{ V}$; $T_{amb} = 25\text{ °C}$; FM-MUTE-ON ($I_7 = 0$); $f_{IF} = 10.7\text{ MHz}$; deviation $\pm 22.5\text{ kHz}$ with $f_m = 400\text{ Hz}$; $V_i = 10\text{ mV}$ (RMS) at pin 20; de-emphasis of $50\text{ }\mu\text{s}$; tuned circuit at pins 10 and 11 aligned for symmetrical stop pulses; measurements taken in Fig.4; unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---|---|---|--------------|------|------|---------------|
| V_P | supply voltage | | 7.5 | 8.5 | 12 | V |
| I_P | supply current | $I_2 = I_7 = 0$ | – | 20 | 26 | mA |
| Mode switch input | | | | | | |
| I_7 | input current | FM-MUTE-ON | – | 0 | – | mA |
| V_7 | input voltage | FM-MUTE-ON | 2.4 | 2.8 | 3.2 | V |
| | | FM-MUTE-OFF | $0.9V_{ref}$ | – | – | V |
| | | FM-OFF; AF attenuation >60 dB | – | – | 1.4 | V |
| IF amplifier and demodulator | | | | | | |
| Z_i | demodulator input impedance between pins 10 and 11 | | 25 | 40 | 55 | k Ω |
| C_i | demodulator input capacitance between pins 10 and 11 | | – | 6 | – | pF |
| AF output (pin 4) | | | | | | |
| R_o | output resistance | | – | 400 | – | Ω |
| V_4 | DC output voltage level | $V_{iIF} \leq 5\text{ }\mu\text{V}$ (RMS) on pin 20 | 2.75 | 3.1 | 3.45 | V |
| PSRR | power supply ripple rejection (pin 4) | $f = 1000\text{ Hz}$; $V_{ripple} = 50\text{ mV}$ (RMS) | 33 | 36 | – | dB |
| Tuning stop detector | | | | | | |
| Δf_{STOP-0} | detuning frequency for STOP-0 (pin 15) | $V_{15} \geq 3.5\text{ V}$; see Fig.11 | – | – | 26 | kHz |
| | | $V_{15} \leq 0.3\text{ V}$; see Fig.11 | 38 | – | – | kHz |
| Δf_{STOP-1} | detuning frequency for STOP-1 (pin 14) | $V_{14} \geq 3.5\text{ V}$; see Fig.10 | – | – | –26 | kHz |
| | | $V_{14} \leq 0.3\text{ V}$; see Fig.10 | –38 | – | – | kHz |
| $V_{20(rms)}$ | dependence on input voltage for STOP-0 and STOP-1 (RMS value) | $V_{14, 15} \geq 3.5\text{ V}$; see Fig.9 | 250 | – | – | μV |
| | | $V_{14, 15} \leq 0.3\text{ V}$; see Fig.9 | – | – | 50 | μV |
| $V_{14, 15}$ | output voltage | $I_{14, 15} = 1\text{ mA}$ | – | – | 0.3 | V |
| Reference voltage source (pin 5) | | | | | | |
| V_{ref} | reference output voltage | $I_5 = -1\text{ mA}$ | 3.3 | 3.7 | 4.1 | V |
| R_5 | output resistance | $I_5 = -1\text{ mA}$ | – | 40 | 80 | Ω |
| TC | temperature coefficient | | – | 3.3 | – | mV/VK |

IF amplifier/demodulator for FM radio receivers

TDA1597

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--------------------------------|---|---|------|------|------|--------|
| External muting | | | | | | |
| V ₁₆ | muting voltage at I ₂ = 0 | V ₂₀ ≤ 5 μV (RMS); see Fig.12 | 1.45 | 1.75 | 2.05 | V |
| | | V ₂₀ = 1 mV (RMS) | 3.0 | 3.45 | 3.9 | V |
| S | steepness of control voltage | slope: 100 μV ≤ V ₂₀ ≤ 100 mV; 20ΔlogV ₂₀ = 20 dB $\frac{\Delta V_{16}}{\Delta \log V_{20}}$ | – | 0.85 | – | V/dec |
| Internal mute α; note 1 | | | | | | |
| α | mute voltage | V ₁₆ ≥ V _{ref} | – | 0 | – | dB |
| | | V ₁₆ = 0.77V _{ref} | 1.5 | – | 4.5 | dB |
| | | V ₁₆ = 0.55V _{ref} | – | 20 | – | dB |
| I ₁₆ | current for capacitor (pin 16) | | | | | |
| | charge current | V ₁₃ = 0 V | – | –8 | – | μA |
| | discharge current | V ₁₃ = 0 V | – | 120 | – | μA |
| | charge current | V ₁₃ = V _{ref} | – | –100 | – | μA |
| | discharge current | V ₁₃ = V _{ref} | – | 120 | – | μA |
| Level detector | | | | | | |
| R ₆ | output resistance pin 6 | | – | – | 500 | Ω |
| V ₆ | output voltage at I ₂ = 0 | V ₂₀ ≤ 5 μV (RMS); see Fig.14 | 0.1 | – | 1.1 | V |
| | | V ₂₀ = 1 mV (RMS) | 3.0 | – | 4.2 | V |
| | | ±200 kHz detuning | 1.2 | 1.5 | 1.8 | V |
| | output voltage at V ₂ = V ₅ | V ₂₀ ≤ 5 μV (RMS) | – | – | 0.3 | V |
| ΔV ₆ | output voltage at detuning | ±45 kHz detuning | – | – | 0.2 | V |
| TC | temperature coefficient | | – | 3.3 | – | mV/VK |
| Δf | detuning frequency | V ₆ = 1.8 V; see Fig.13 | 90 | – | 160 | kHz |
| S | steepness of control voltage | slope: 50 μV ≤ V ₂₀ ≤ 50 mV; 20ΔlogV ₂₀ = 20 dB $\frac{\Delta V_6}{\Delta \log V_{20}}$ | 1.4 | 1.7 | 2.0 | V/dec |
| ΔV ₆ /Δf | slope of output voltage at detuning | Δf = 125 ±20 kHz | – | 35 | – | mV/kHz |
| S | level shift adjustments | | | | | |
| | range by pin 2 | ±ΔV ₆ /V _{ref} | 0.42 | 0.5 | – | V/V |
| | gain | –ΔV ₆ /ΔV ₂ | – | 1.7 | – | V/V |
| | range by pin 2 | ±ΔV ₁₆ /V _{ref} | 0.21 | 0.25 | – | V/V |
| | gain | –ΔV ₁₆ /ΔV ₂ | – | 0.85 | – | V/V |

Note

$$1. \alpha = 20 \log \frac{\Delta V_{4(\text{FM-MUTE-OFF})}}{\Delta V_{4(\text{FM-MUTE-ON})}}$$

IF amplifier/demodulator for FM radio receivers

TDA1597

OPERATING CHARACTERISTICS

$V_P = 7.5$ to 12 V; $T_{amb} = 25$ °C; FM-MUTE-ON ($I_7 = 0$); $f_{IF} = 10.7$ MHz; deviation ± 22.5 kHz with $f_m = 400$ Hz; $V_i = 10$ mV (RMS) at pin 20; de-emphasis of 50 μ s; tuned circuit at pins 10 and 11 aligned for symmetrical stop pulses; measurements taken in Fig.4; unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---|---|--|--------|----------|--------|--------------------|
| IF amplifier and demodulator | | | | | | |
| $V_{i(rms)}$ | input signal for start of limiting (-3 dB) (RMS value; pin 20) | $V_7 = V_{ref}$; FM-MUTE-OFF | 14 | 22 | 35 | μ V |
| | input signal for signal-to-noise ratio (RMS value) | $f = 250$ to 15000 Hz; $V_7 = V_{ref}$ S/N = 26 dB S/N = 46 dB | – – | 15 60 | – – | μ V μ V |
| S/N | signal-to-noise ratio | deviation ± 75 kHz | – | 82 | – | dB |
| $V_{o(rms)}$ | AF output signal (RMS value; pin 4) | | 180 | 200 | 220 | mV |
| THD | total harmonic distortion without de-emphasis | | | | | |
| | without detuning | deviation ± 75 kHz; $f_m = 1$ kHz; $I_7 = 0$ | – | 0.1 | 0.3 | % |
| | ± 25 kHz detuning | deviation ± 75 kHz; $f_m = 1$ kHz; $I_7 = 0$ | – | – | 0.6 | % |
| | compensated via pin 3 | deviation ± 75 kHz; $f_m = 1$ kHz; $I_7 = 0$; $V_7 = V_{ref}$ | – | 0.07 | 0.25 | % |
| ΔV_4 | K2 adjustment $\Delta V_4 = V_4 (V_3 = 0) - V_4 (V_3 = V_{ref})$ | | 10 | – | – | mV |
| α_{AM} | AM suppression on pin 4 $V_i = 0.3$ to 1000 mV (RMS) | $V_7 = V_{ref}$; $m = 30\%$ on pin 20 | 46 | 55 | – | dB |
| | $V_i = 1$ to 300 mV (RMS) | on pin 20 | 60 | 65 | – | dB |
| Dynamic mute attenuation α; note 1 | | | | | | |
| α | dynamic mute attenuation | deviation ± 75 kHz; $f_m = 100$ kHz; $V_2 = 1$ V | – | 14 | – | dB |

IF amplifier/demodulator for FM radio receivers

TDA1597

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---|---|--|------|------|------|------------------|
| Tuning stop detector | | | | | | |
| $\Delta f_{\text{STOP-0}}$ | detuning frequency for STOP-0 (pin 15) | $V_{15} \geq 3.5 \text{ V}$; see Fig.11 | – | – | 26 | kHz |
| | | $V_{15} \leq 0.3 \text{ V}$; see Fig.11 | 38 | – | – | kHz |
| $\Delta f_{\text{STOP-1}}$ | detuning frequency for STOP-1 (pin 14) | $V_{14} \geq 3.5 \text{ V}$; see Fig.10 | – | – | –26 | kHz |
| | | $V_{14} \leq 0.3 \text{ V}$; see Fig.10 | –38 | – | – | kHz |
| $V_{20(\text{rms})}$ | dependence on input voltage for STOP-0 and STOP-1 (RMS value) | $V_{14, 15} \geq 3.5 \text{ V}$; see Fig.9 | 250 | – | – | μV |
| | | $V_{14, 15} \leq 0.3 \text{ V}$; see Fig.9 | – | – | 50 | μV |
| R_8 | internal low-pass resistance of detune detector | | 12 | 25 | 50 | $\text{k}\Omega$ |
| V_8 | voltage on capacitor | $V_i \leq 5 \mu\text{V}$ (RMS) on input pin 20; $I_7 = 0$ | – | 2.2 | – | V |
| Level detector ($I_2 = 0$) | | | | | | |
| V_6 | output voltage | $V_{20} \leq 5 \mu\text{V}$ (RMS) | 0.1 | – | 1.1 | V |
| | | $V_{20} = 1 \text{ mV}$ (RMS) | 3.0 | – | 4.2 | V |
| Reference voltage source (pin 5) | | | | | | |
| V_{ref} | reference output voltage | $I_5 = -1 \text{ mA}$ | 3.3 | 3.7 | 4.1 | V |
| Operation with AM-IF | | | | | | |
| Level and stop information (on pins 6, 13, 14, 15 and 16) is provided for the modes FM-MUTE-ON and FM-MUTE-OFF. This information is also available in the FM-OFF mode when an AM-IF signal is input (for example 455 kHz). This can also provide a valid detuning information when a suitable AM-IF resonance circuit is provided for demodulator (see Fig.18). | | | | | | |

Note

$$1. \alpha = 20 \log \frac{\Delta V_{4(\text{FM-MUTE-OFF})}}{\Delta V_{4(\text{FM-MUTE-ON})}}$$

IF amplifier/demodulator for FM radio receivers

TDA1597

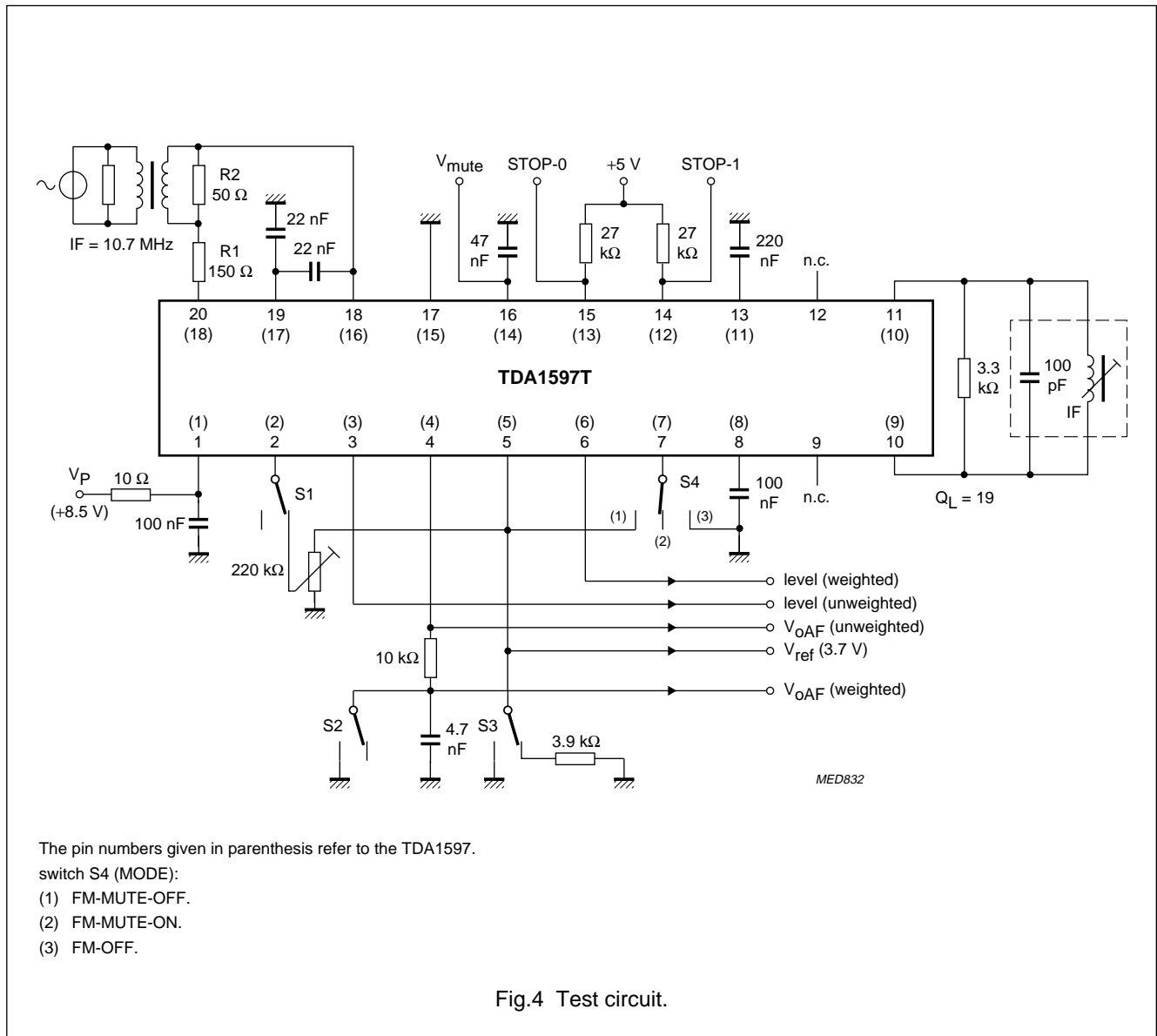
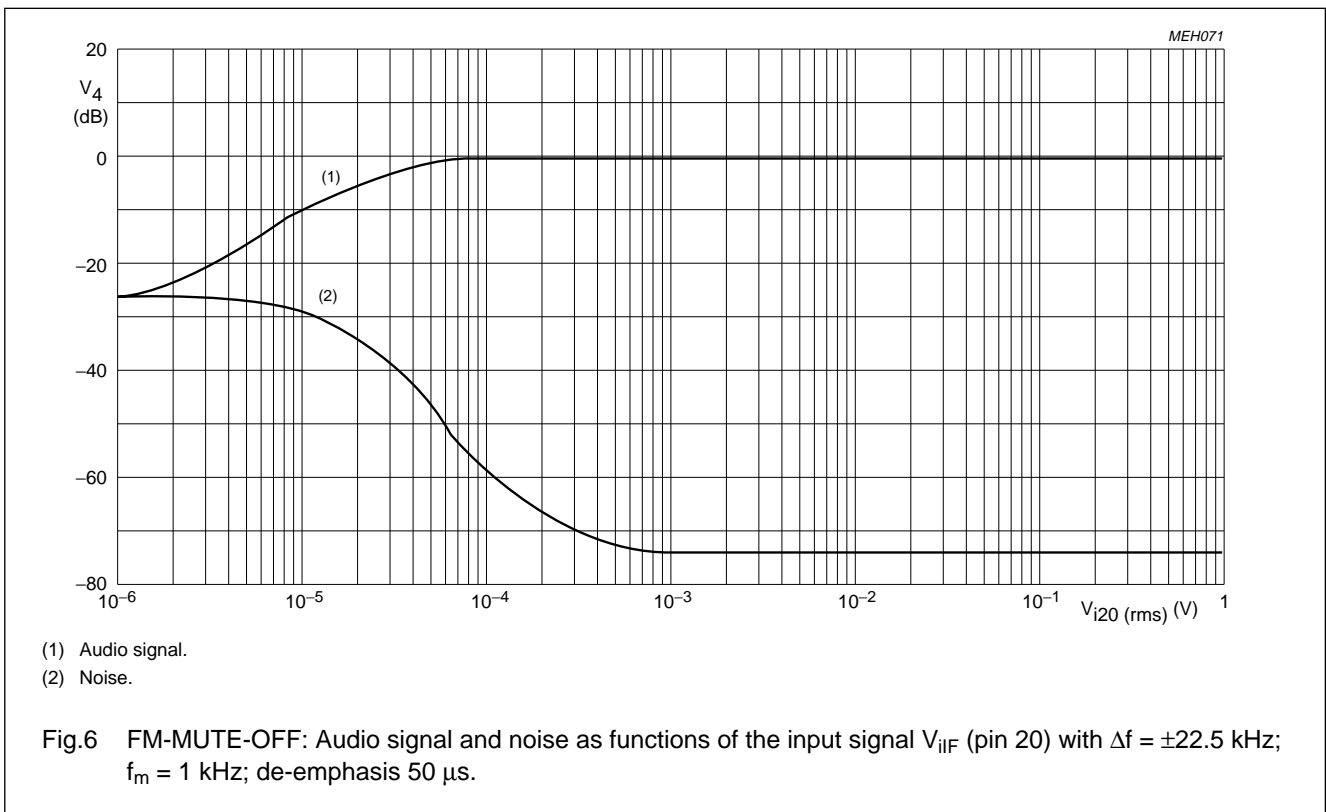
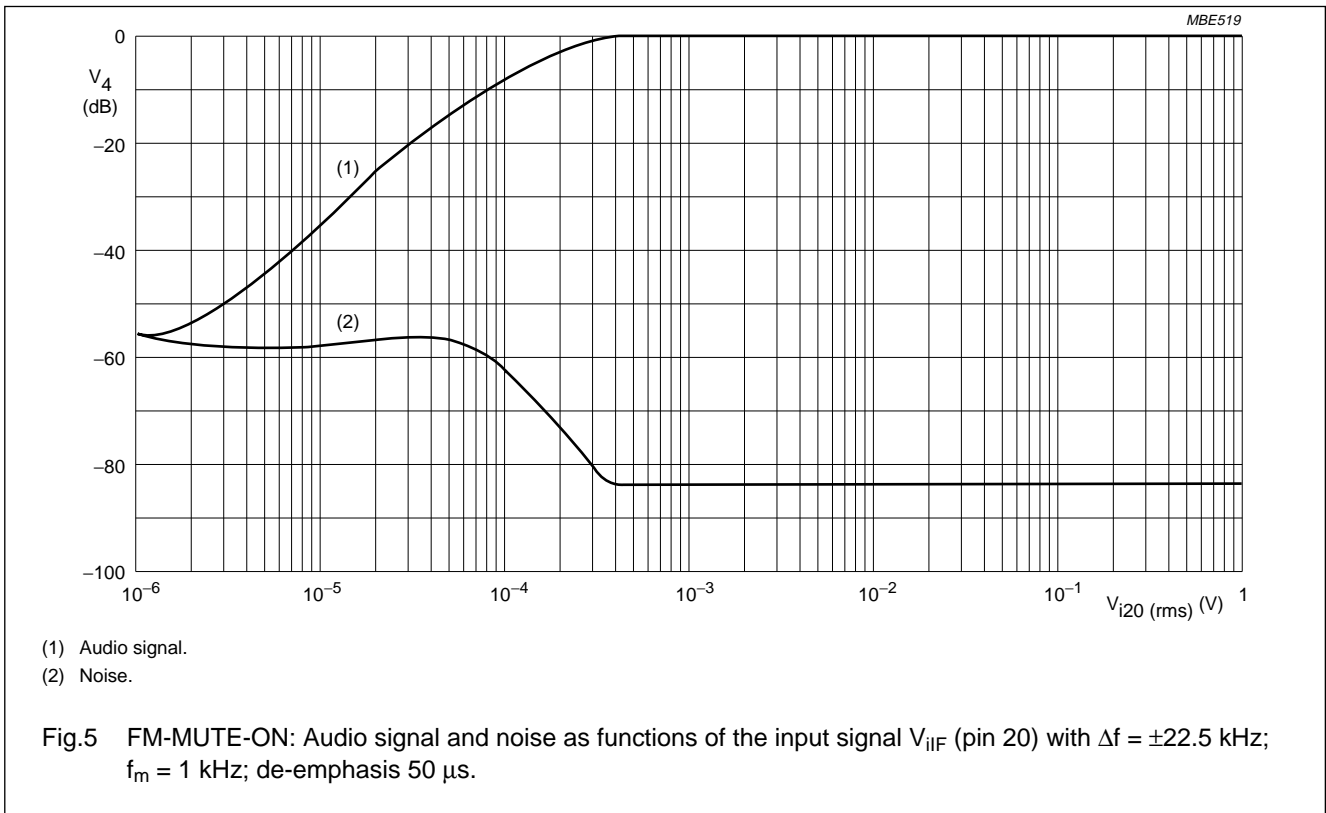


Fig.4 Test circuit.

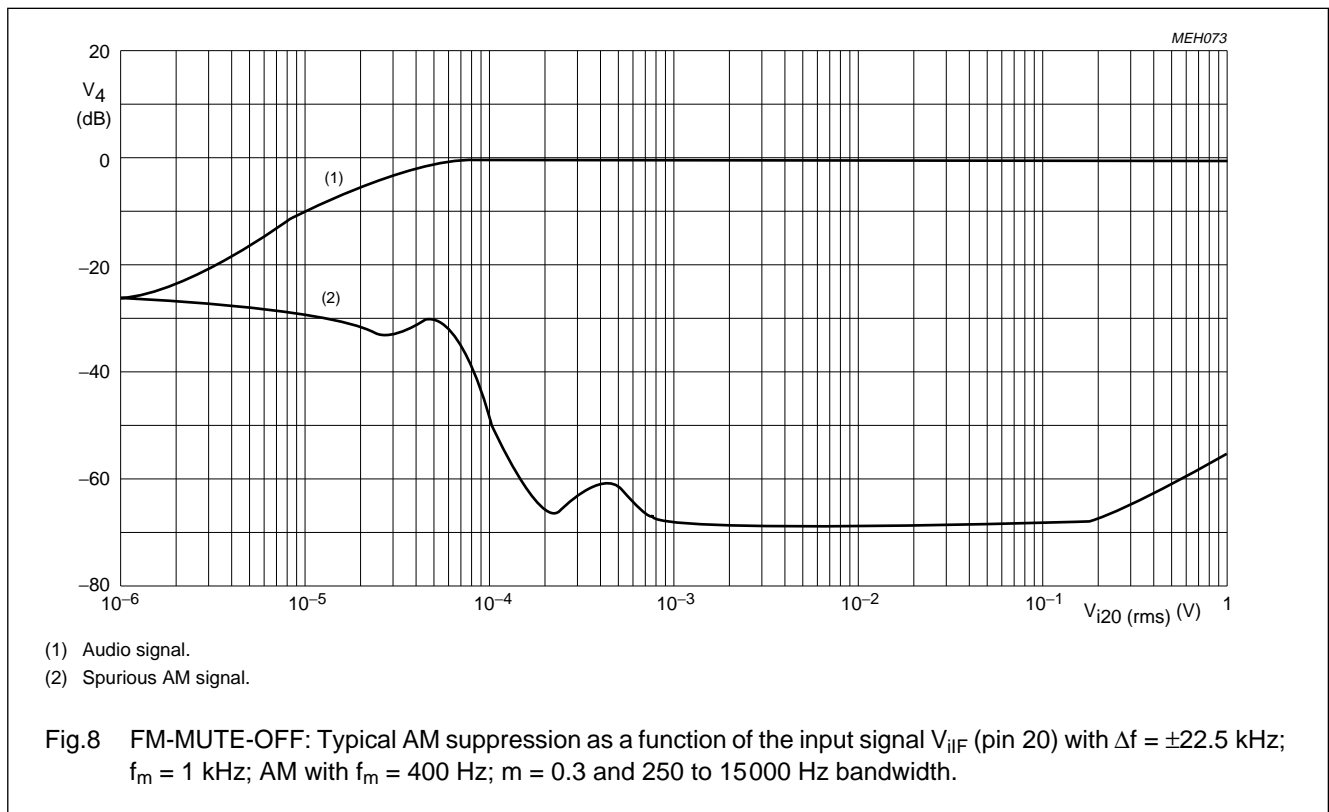
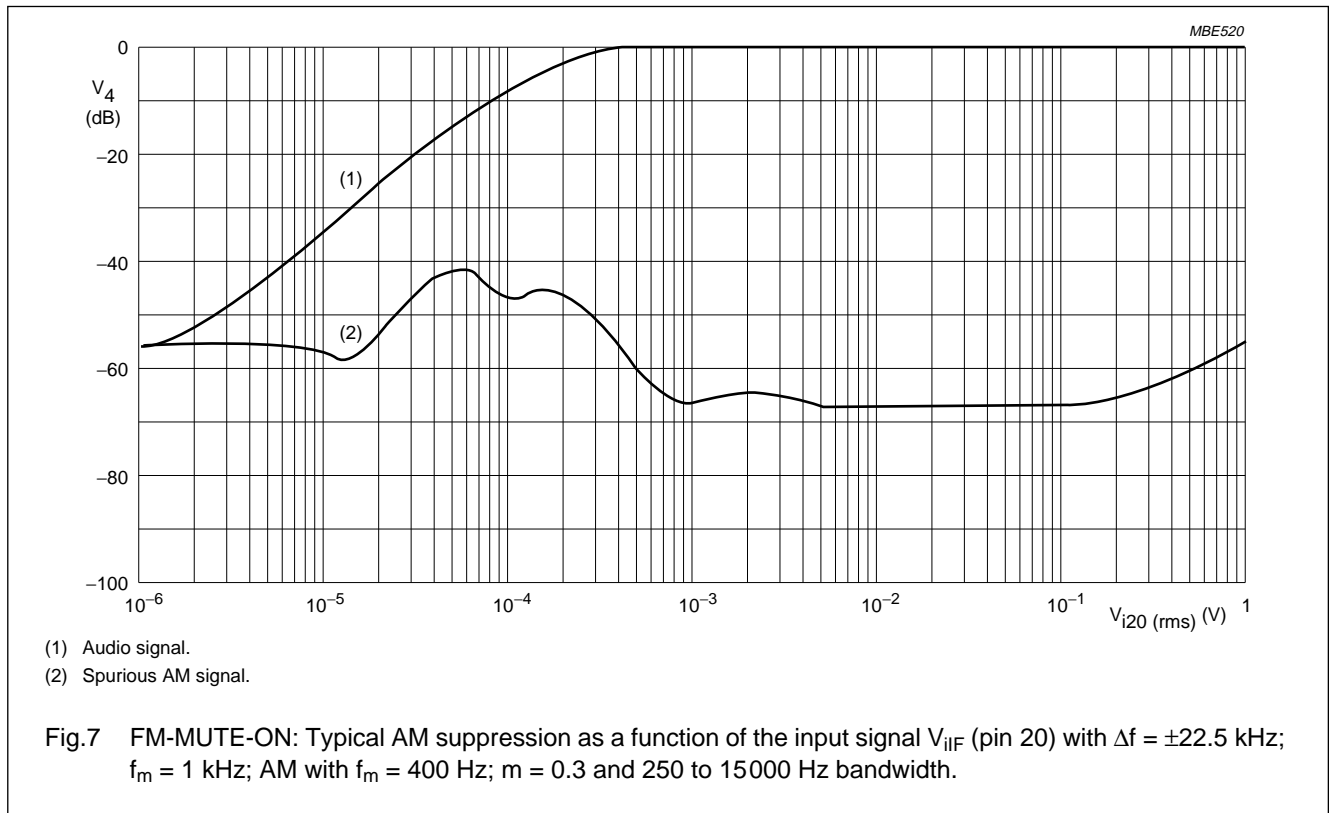
IF amplifier/demodulator for FM radio receivers

TDA1597



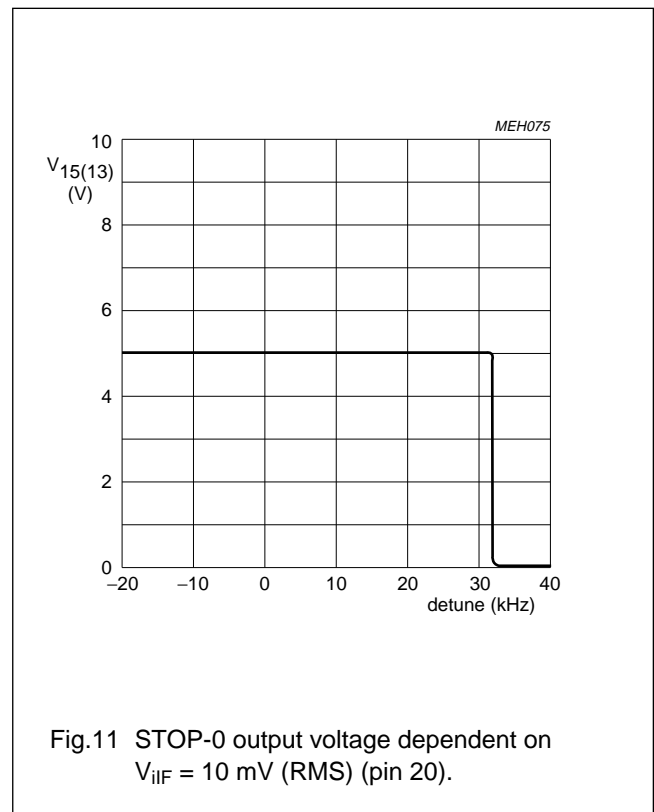
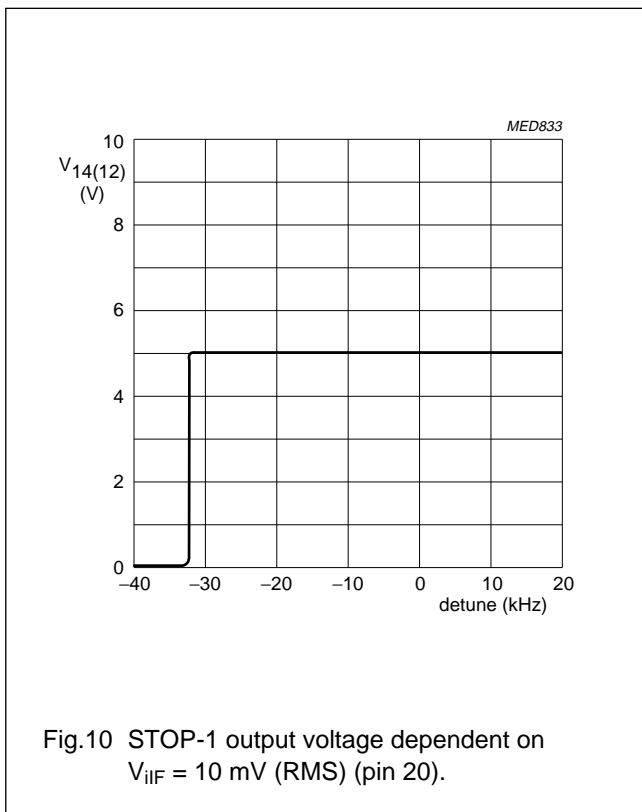
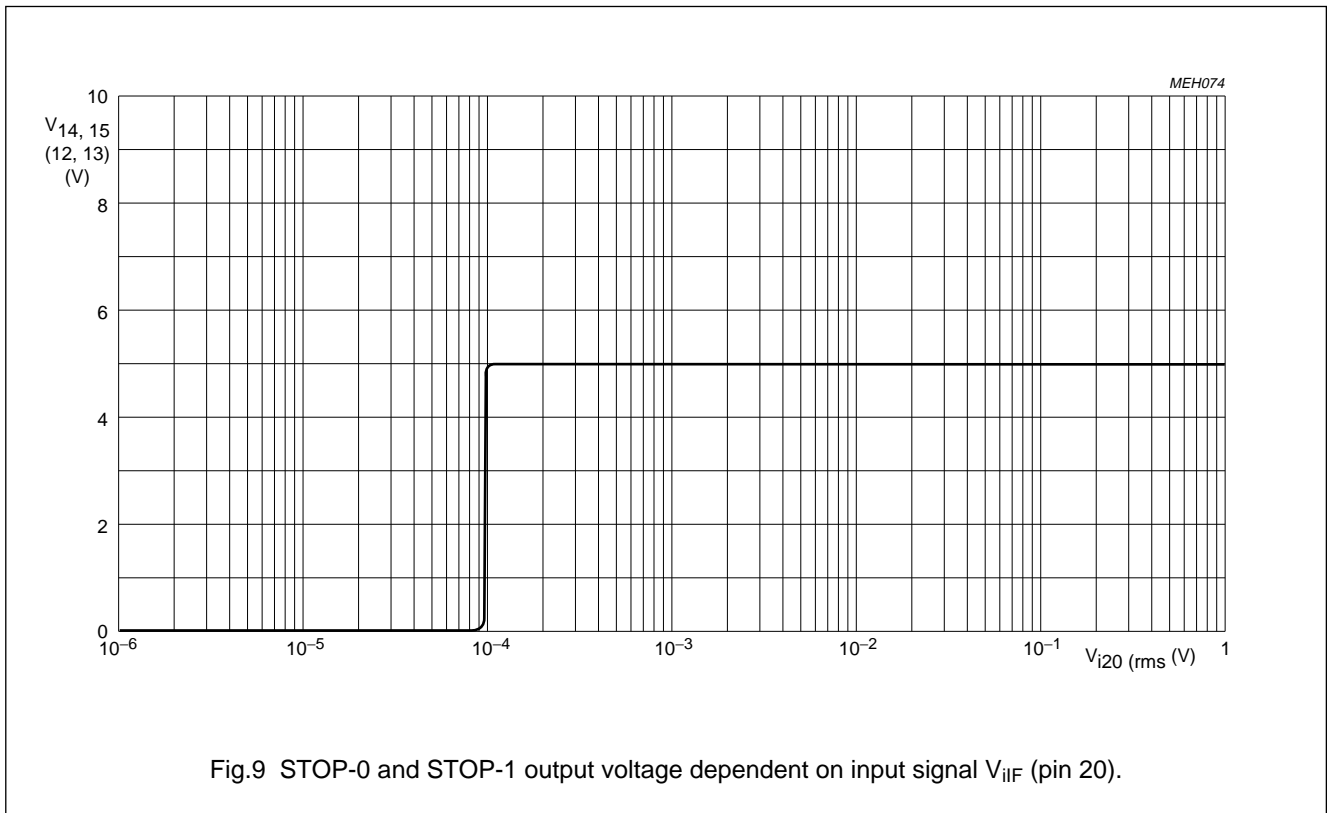
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TDA1597



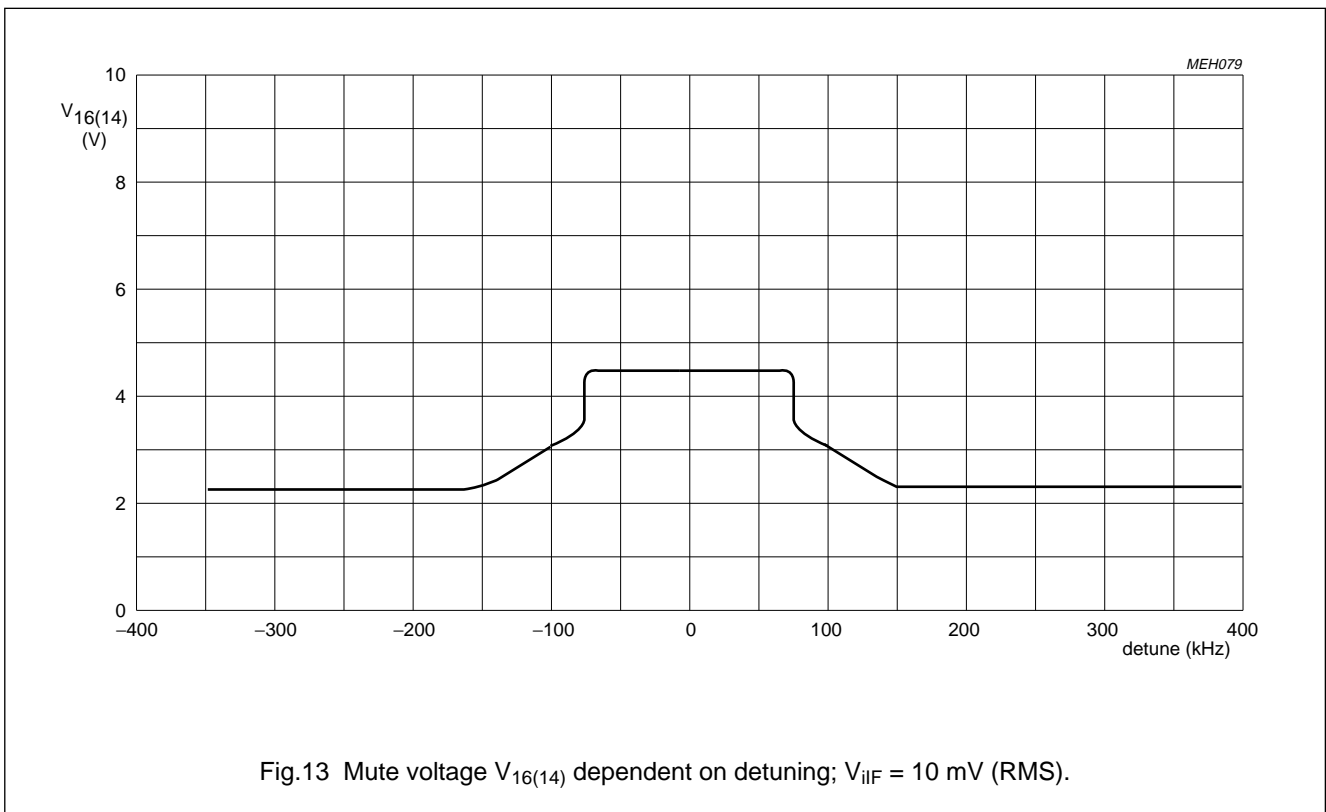
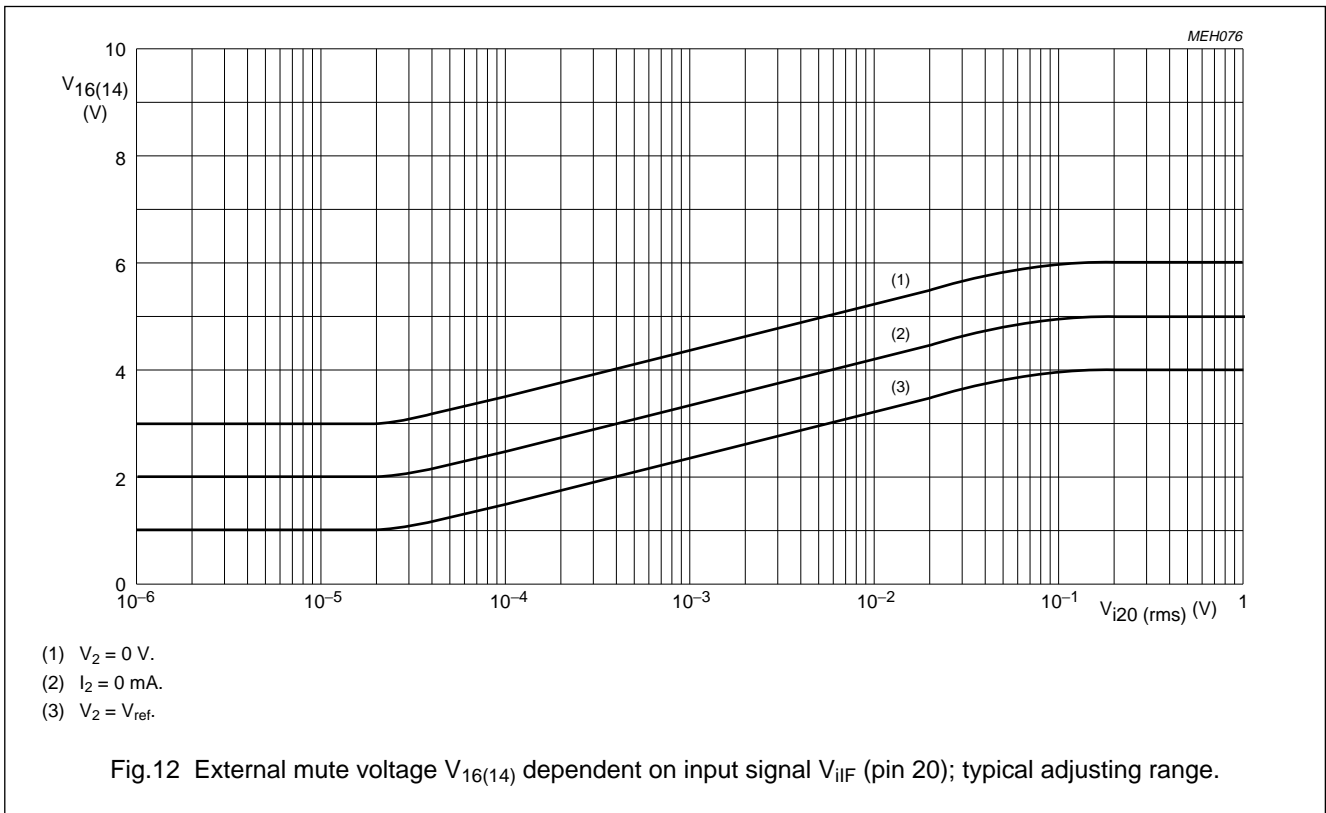
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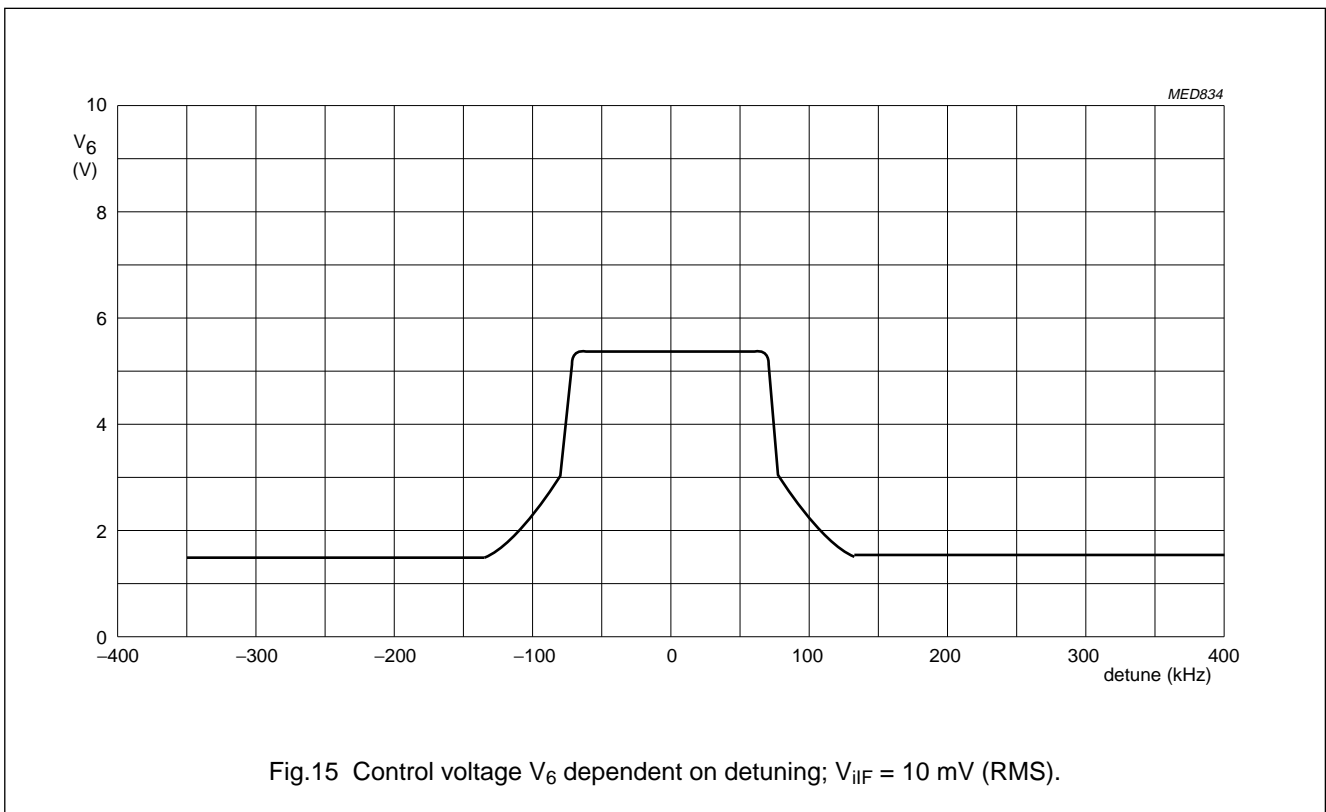
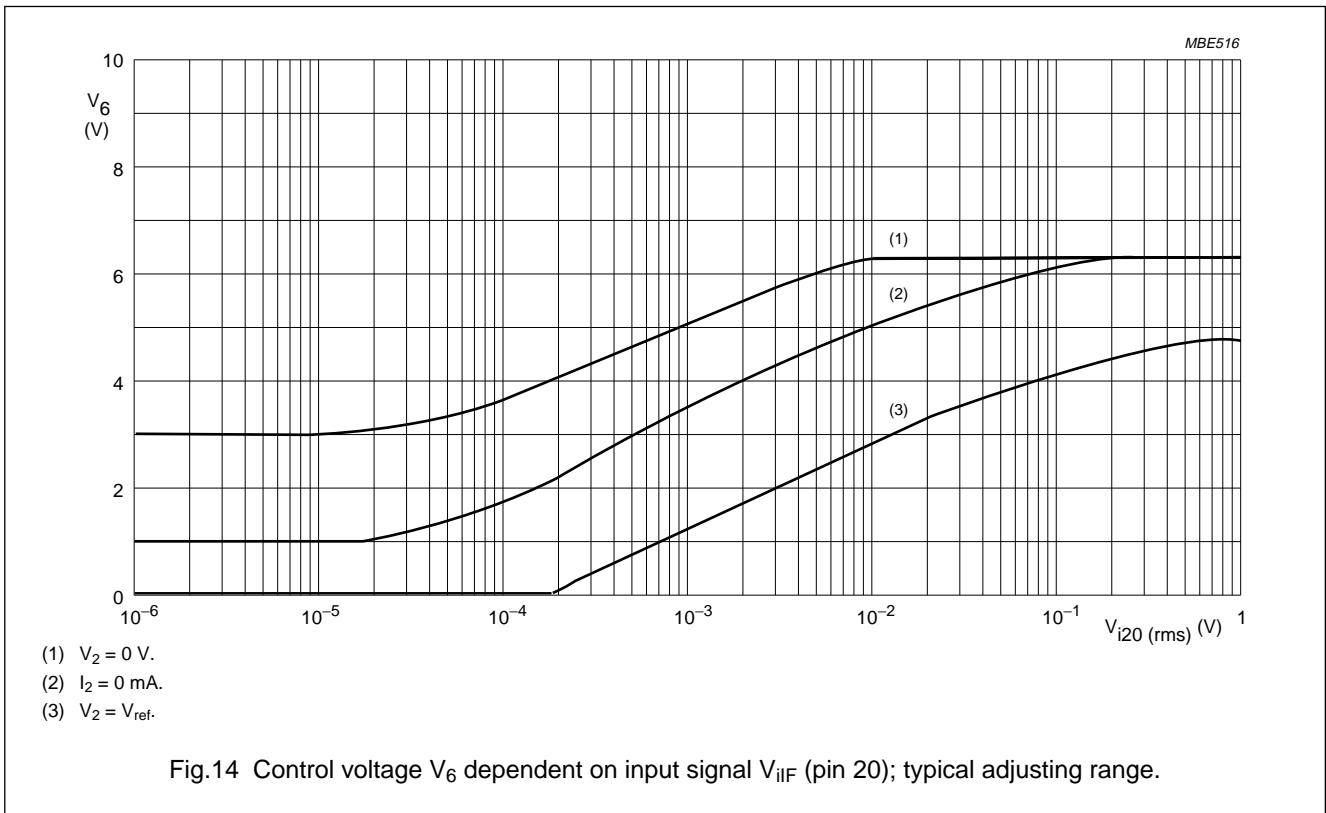
IF amplifier/demodulator for FM radio receivers

TDA1597



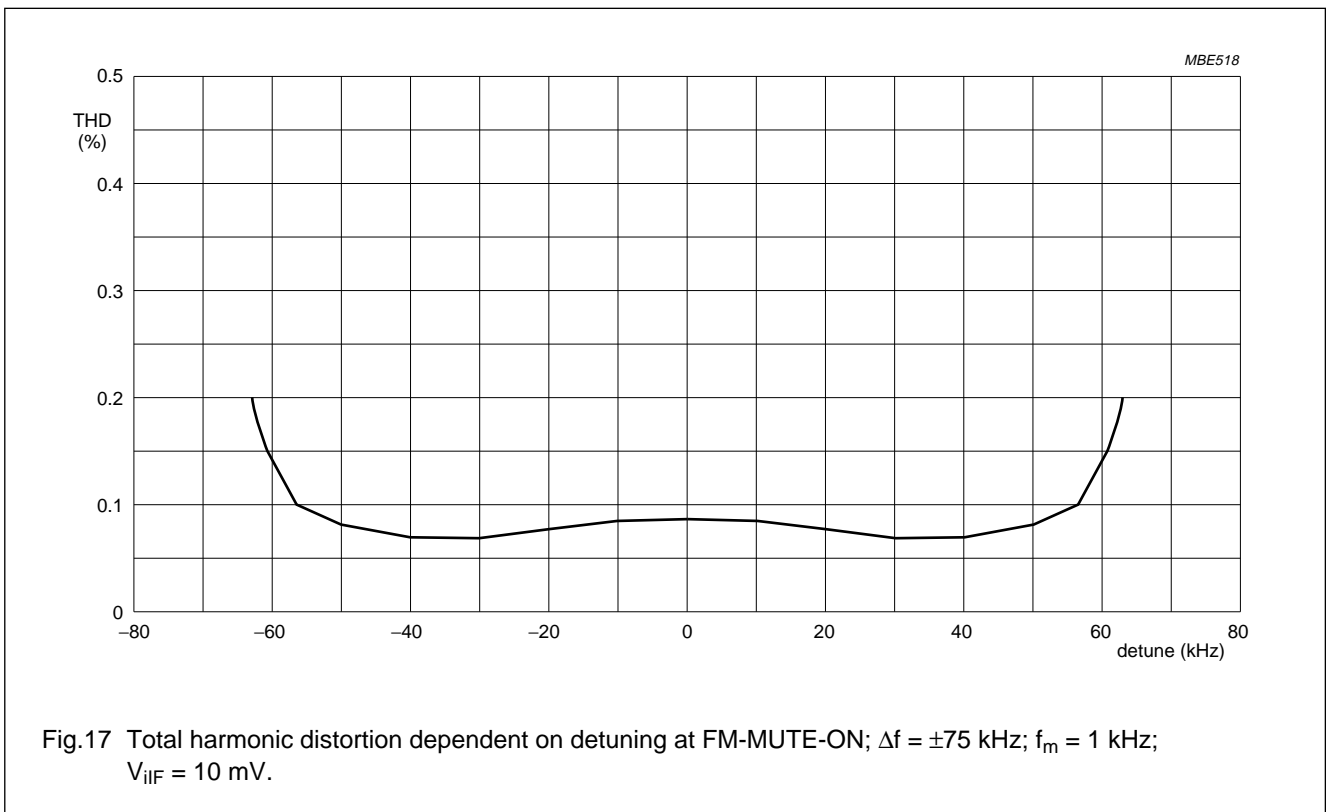
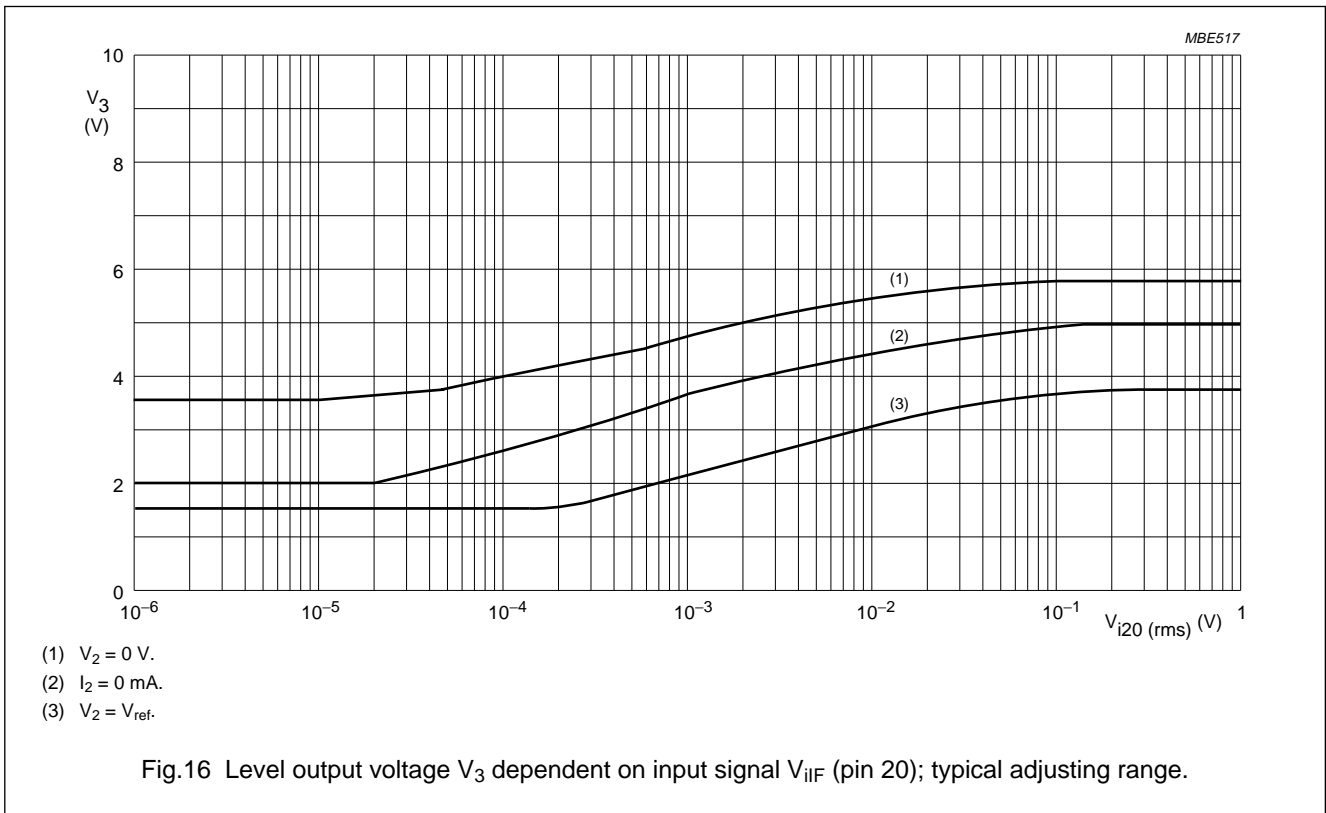
IF amplifier/demodulator for FM radio receivers

TDA1597



IF amplifier/demodulator for FM radio receivers

TDA1597



IF amplifier/demodulator for FM radio receivers

TDA1597

APPLICATION INFORMATION

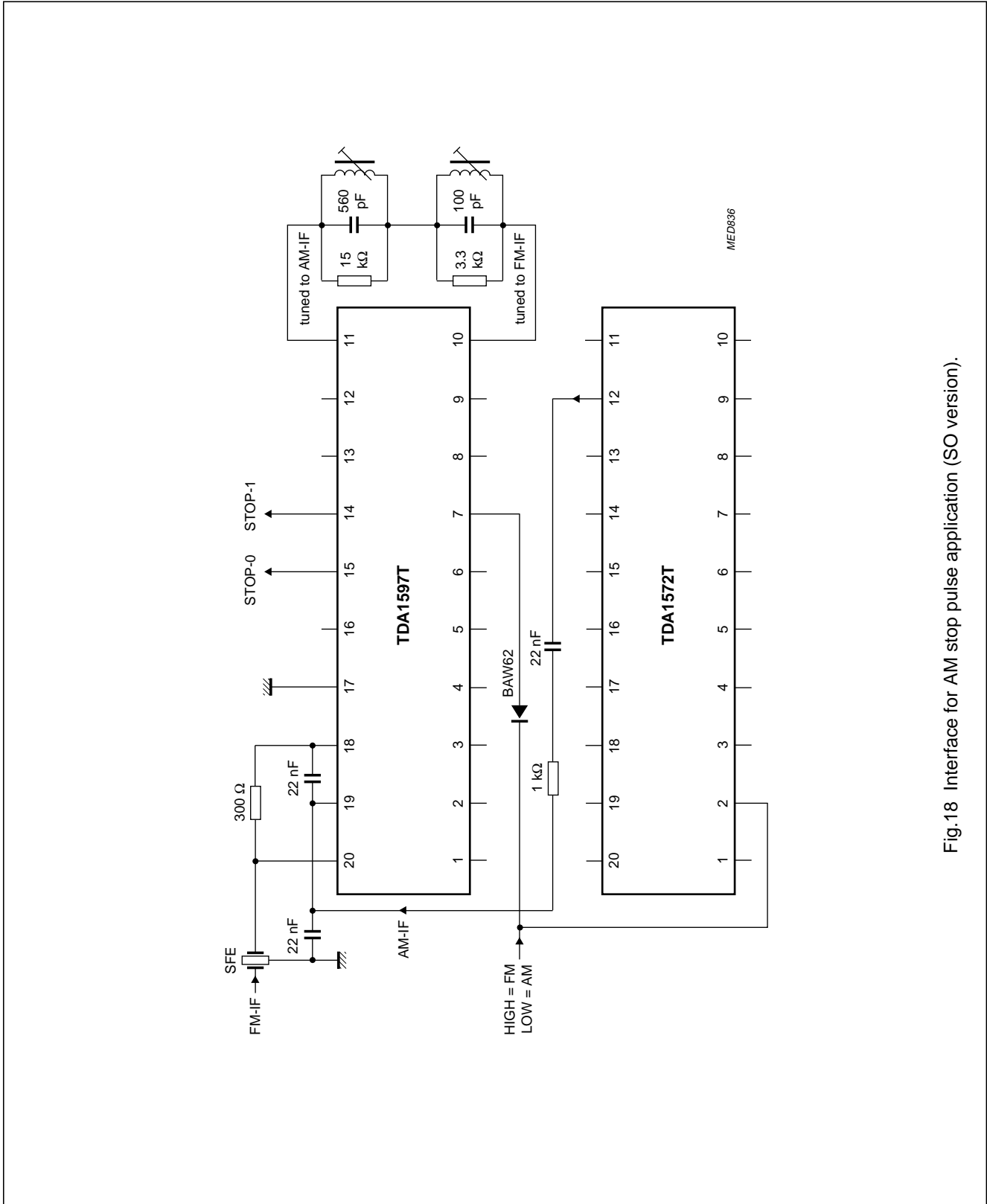


Fig.18 Interface for AM stop pulse application (SO version).

IF amplifier/demodulator for FM radio receivers

TDA1597

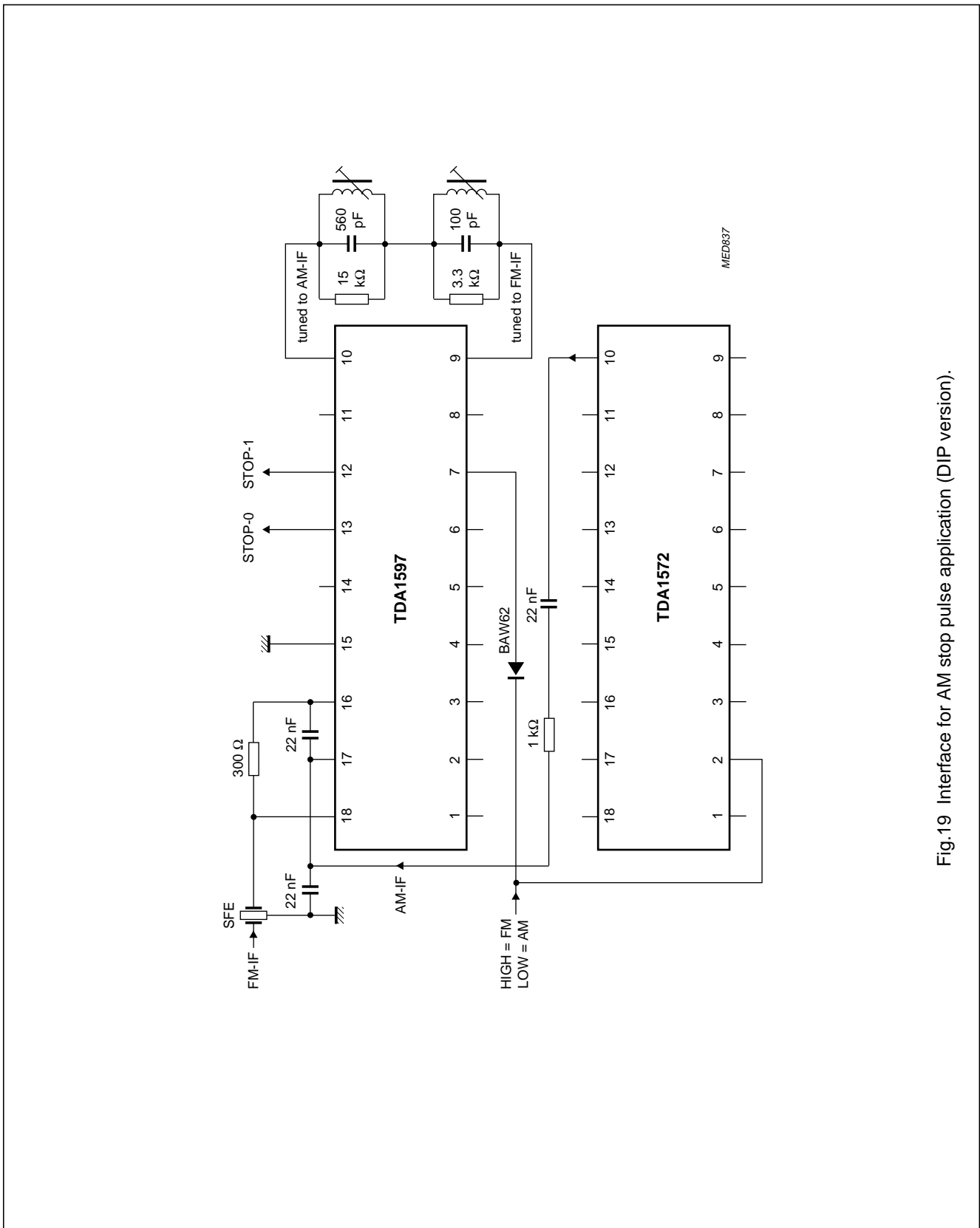


Fig.19 Interface for AM stop pulse application (DIP version).

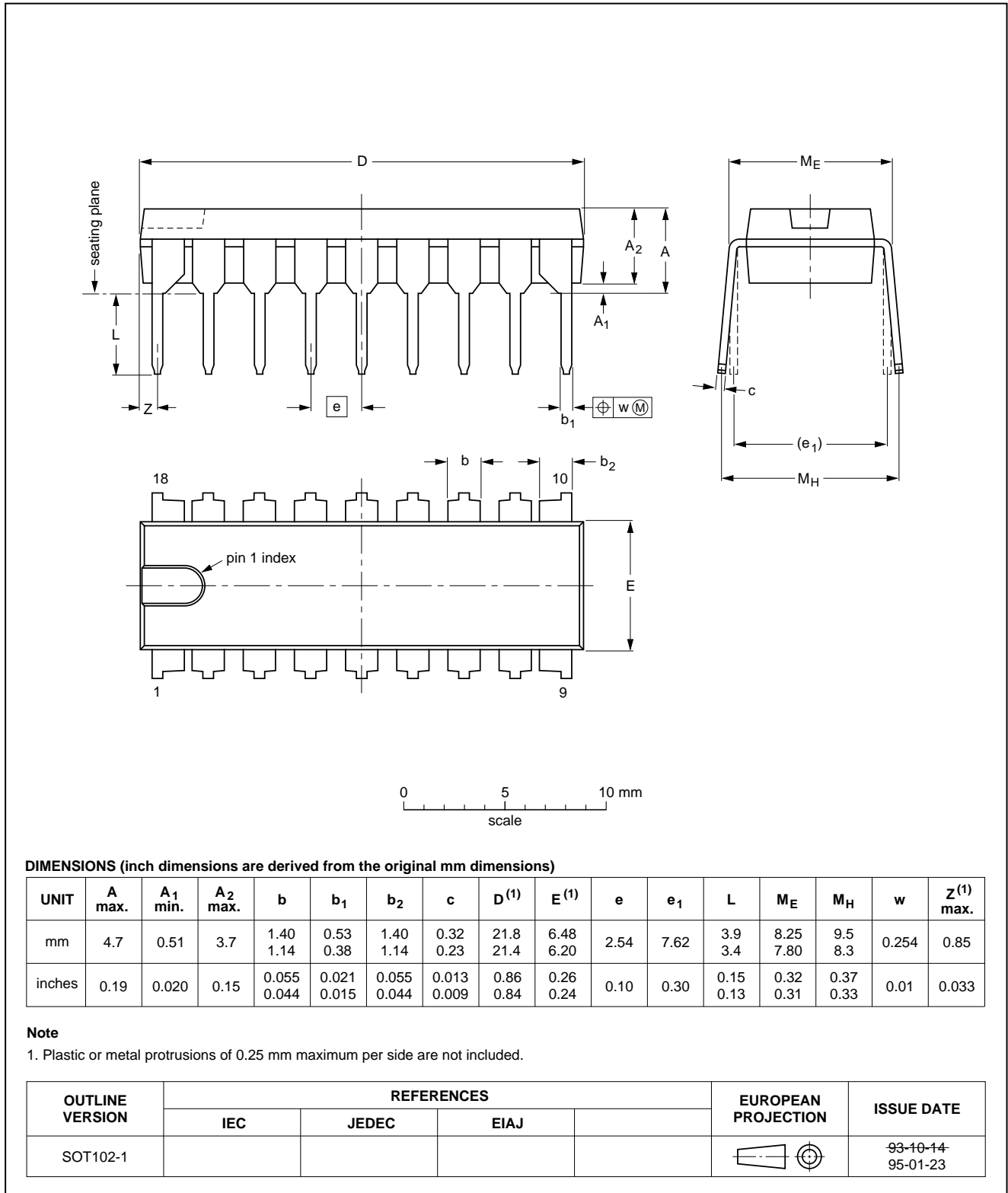
IF amplifier/demodulator for FM radio receivers

TDA1597

PACKAGE OUTLINES

DIP18: plastic dual in-line package; 18 leads (300 mil)

SOT102-1

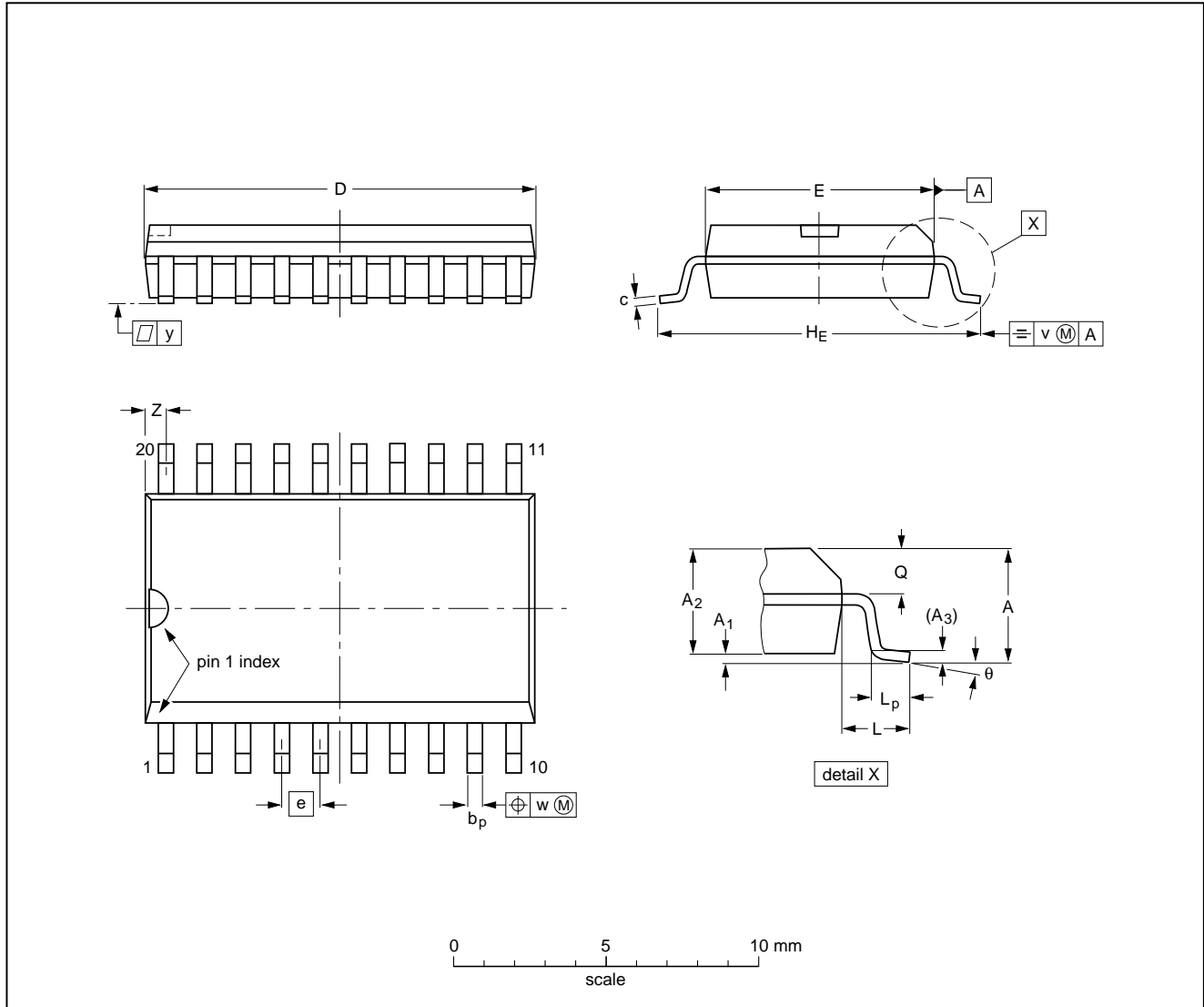


IF amplifier/demodulator for FM radio receivers

TDA1597

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

| UNIT | A max. | A ₁ | A ₂ | A ₃ | b _p | c | D ⁽¹⁾ | E ⁽¹⁾ | e | H _E | L | L _p | Q | v | w | y | Z ⁽¹⁾ | θ |
|--------|--------|----------------|----------------|----------------|----------------|----------------|------------------|------------------|-------|----------------|-------|----------------|----------------|------|------|-------|------------------|----------|
| mm | 2.65 | 0.30 0.10 | 2.45 2.25 | 0.25 | 0.49 0.36 | 0.32 0.23 | 13.0 12.6 | 7.6 7.4 | 1.27 | 10.65 10.00 | 1.4 | 1.1 0.4 | 1.1 1.0 | 0.25 | 0.25 | 0.1 | 0.9 0.4 | 8° 0° |
| inches | 0.10 | 0.012 0.004 | 0.096 0.089 | 0.01 | 0.019 0.014 | 0.013 0.009 | 0.51 0.49 | 0.30 0.29 | 0.050 | 0.42 0.39 | 0.055 | 0.043 0.016 | 0.043 0.039 | 0.01 | 0.01 | 0.004 | 0.035 0.016 | |

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|-----------------|------------|----------|------|--|---------------------|----------------------|
| | IEC | JEDEC | EIAJ | | | |
| SOT163-1 | 075E04 | MS-013AC | | | | 92-11-17 95-01-24 |

IF amplifier/demodulator for FM radio receivers
TDA1597**SOLDERING****Plastic dual in-line packages**

BY DIP OR WAVE

The maximum permissible temperature of the solder is 260 °C; this temperature must not be in contact with the joint for more than 5 s. The total contact time of successive solder waves must not exceed 5 s.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified storage maximum. If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

REPAIRING SOLDERED JOINTS

Apply a low voltage soldering iron below the seating plane (or not more than 2 mm above it). If its temperature is below 300 °C, it must not be in contact for more than 10 s; if between 300 and 400 °C, for not more than 5 s.

Plastic small outline packages

BY WAVE

During placement and before soldering, the component must be fixed with a droplet of adhesive. After curing the adhesive, the component can be soldered. The adhesive can be applied by screen printing, pin transfer or syringe dispensing.

Maximum permissible solder temperature is 260 °C, and maximum duration of package immersion in solder bath is 10 s, if allowed to cool to less than 150 °C within 6 s. Typical dwell time is 4 s at 250 °C.

A modified wave soldering technique is recommended using two solder waves (dual-wave), in which a turbulent wave with high upward pressure is followed by a smooth laminar wave. Using a mildly-activated flux eliminates the need for removal of corrosive residues in most applications.

BY SOLDER PASTE REFLOW

Reflow soldering requires the solder paste (a suspension of fine solder particles, flux and binding agent) to be applied to the substrate by screen printing, stencilling or pressure-syringe dispensing before device placement.

Several techniques exist for reflowing; for example, thermal conduction by heated belt, infrared, and vapour-phase reflow. Dwell times vary between 50 and 300 s according to method. Typical reflow temperatures range from 215 to 250 °C.

Preheating is necessary to dry the paste and evaporate the binding agent. Preheating duration: 45 min at 45 °C.

REPAIRING SOLDERED JOINTS (BY HAND-HELD SOLDERING IRON OR PULSE-HEATED SOLDER TOOL)

Fix the component by first soldering two, diagonally opposite, end pins. Apply the heating tool to the flat part of the pin only. Contact time must be limited to 10 s at up to 300 °C. When using proper tools, all other pins can be soldered in one operation within 2 to 5 s at between 270 and 320 °C. (Pulse-heated soldering is not recommended for SO packages.)

For pulse-heated solder tool (resistance) soldering of VSO packages, solder is applied to the substrate by dipping or by an extra thick tin/lead plating before package placement.

IF amplifier/demodulator for FM radio receivers

TDA1597

DEFINITIONS

| | |
|---|---|
| Data sheet status | |
| Objective specification | This data sheet contains target or goal specifications for product development. |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification | This data sheet contains final product specifications. |
| Limiting values | |
| Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability. | |
| Application information | |
| Where application information is given, it is advisory and does not form part of the specification. | |

LIFE SUPPORT APPLICATIONS

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IF amplifier/demodulator for FM radio receivers

TDA1597

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